

Listing Of The Claims:

1. (Currently amended) A method of changing a driving sequence to output a charge ~~couple~~ coupled device signal, applied to ~~[[a]]an apparatus -scanner~~ having a pixel processor and a charge ~~couple~~ coupled device, wherein a plurality of charge signals ~~detected~~ formed by the charge ~~couple~~ coupled device are sequentially sent to the pixel processor according to ~~the~~ a first driving sequence, and the pixel processor outputs the charge signals according to a sampling sequence, the method comprising:

~~providing a fast driving sequence with a period less than the above driving sequence;~~

sending the charge signals to the pixel processor according to ~~the fast~~ a second driving sequence with a period less than the first driving sequence; ~~and~~

sampling the charge signals by the pixel processor according to the sampling sequence;, and

outputting data obtained by sampling.

2. (Currently amended) The method according to claim 1, wherein an initial position of ~~the fast~~ said first driving sequence is shifted with a phase, and the pixel processor samples and outputs the charge signals at different positions.

3. (Previously presented) A method of changing a driving sequence to send a plurality of charge signals sequentially to a pixel processor according to the driving sequence, while the pixel processor outputs the charge signals according to a sampling sequence, the method comprising:

decreasing a period of the driving sequence;

sending the charge signals to the pixel processor according to the driving sequence; and

sampling the charge signals by the pixel processor according to an unchanged sampling sequence.

4. (Previously presented) The method of claim 3, further comprising:

shifting an initial position of the driving sequence in phase.

5. (Previously presented) The method of claim 4, further comprising:

sampling the charge signals at different positions in the driving sequence; and
outputting the charge signals at different positions in the driving sequence.

6. (Previously presented) The method of claim 4, further comprising:

determining a sensor cell that is in an initial position to output a charge signal.

7. (Previously presented) The method of claim 3, wherein the period of the driving sequence is decreased to half of an initial value.

8. (Currently amended) ~~An apparatus to change a driving sequence to send a plurality of charge signals sequentially to a pixel processor according to the driving sequence, while the pixel processor outputs the charge signals according to a sampling sequence, the apparatus comprising:~~

means for ~~decreasing~~ changing a period of ~~[[the]]~~ a driving sequence;

means for sending ~~[[the]]~~ a plurality of charge signals to ~~a~~ [[the]] pixel processor according to the driving sequence;

and

means for sampling the charge signals by the pixel processor according to an unchanged sampling sequence.

9. (Previously presented) The apparatus of claim 8, further comprising:

means for shifting an initial position of the driving sequence in phase.

10. (Currently amended) The ~~apparatus method~~ of claim 9, further comprising:

means for sampling the charge signals at different positions in the driving sequence; and
means for outputting the charge signals at different positions in the driving sequence.

11. (Currently amended) The apparatus method of claim 9, further comprising:

means for determining a sensor cell that is in an initial position to output a charge signal.

12. (Currently amended) The apparatus method of claim 8, wherein changing the period of the driving sequence comprises decreasing the period of the driving sequence to half of an initial value~~the period of the driving sequence is decreased to half of an initial value.~~

13. (New) The method of claim 2, wherein said apparatus comprises a scanner.

14. (New) The method of claim 1, wherein the second driving sequence has a period of one half the period of the first driving sequence.

15. (New) A system comprising:

an image sensor operable to output a first signal based on a first sequence, wherein said first sequence can be changed;

a pixel processor operable to receive the first signal, said pixel processor further operable to generate a second signal based on a second driving sequence; and

wherein said pixel processor is further operable to continue to generate said second signal based on said second sequence if said first sequence is changed.

16. (New) The system of claim 15, wherein said image sensor comprises a charge coupled device.

17. (New) The system of claim 15, wherein said image sensor is further operable to output said first signal based on a phase shifted driving sequence.

18. (New) The system of claim 15, wherein said pixel processor further operable to sample said first signal at a different position in said driving sequence.

19. (New) The system of claim 15, wherein said image sensor is further operable to output a first signal based on a changed first sequence, wherein said changed first sequence has a period of one half of a period of said first sequence.

20. (New) A method comprising:

changing a period of a first sequence of a image capture device;

sending a signal produced by said image capture device according to the changed first sequence to a pixel processor; and

sampling the sent signal with said pixel processor according to an unchanged second sequence.

21. (New) The method of claim 20, wherein said image capture device comprises a charge coupled device.

22. (New) The method of claim 20, and further comprising: shifting an initial position of said first sequence in phase.

23. (New) The method of claim 22, and further comprising:

sending the signals at different positions in said first sequence; and

sampling the sent signal at a different position in said first sequence.

24. (New) The method of claim 22, further comprising: determining a sensor cell of said image capture device that is in an initial position to output a signal.

25. (New) The method of claim 20, wherein said changing a period of a first sequence comprises decreasing said period to half of an initial value.